

Amendments to the claims:

1. (Previously Presented) A method to write in flash type memory of an electronic module comprising:
 - defining a mirror area in the flash type memory divided into at least two physical areas each designated to correspond to a same logical area for storing content written to the logical area;
 - designating one of the at least two physical areas as being an active physical area; and
 - during a write to said logical area, programming the content of said logical area into the active physical area.
2. (Previously Presented) The method according to claim 1, further comprising:
 - erasing the content of all physical areas in a memory area in a single operation at a convenient time.
3. (Previously Presented) The method according to claim 2, wherein the convenient time is a period of inactivity or when all the physical areas are used.
4. (Previously Presented) The method according to claim 1, comprising copying the active physical area into a buffer area, erasing all physical areas and copying the buffer into a first available physical area in the mirror area.
5. (Previously Presented) The method according to claim 2 comprising performing the erasure and programming/read operations in parallel thereby not blocking the electronic module.
6. (Previously Presented) The method according to claim 5, comprising:
 - performing the erasure and programming/read operations in parallel in a bi-bank memory, said bi-bank memory corresponding to the mirror memory area each bank having physical area(s), one bank being used for programming/reading while the other bank is erased,

changing active bank when all physical areas of the bank used for programming/read have been used.

7. (Previously Presented) The method according to claim 1 comprising designating said active physical areas using a counter and incrementing the counter on each change of active area.
8. (Previously Presented) The method according to claim 1 comprising associating at least one bit with a logical area to represent the use state of at least one physical area of said logical area.
9. (Previously Presented) The method according to claim 1 wherein if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure, the write is carried out in an active physical area and in a blank physical area in the mirror area otherwise.
10. (Previously Presented) The method according to claim 9, comprising programming only a portion of the logical area in the blank physical area.
11. (Cancel)
12. (Cancel)
13. (Cancel)
14. (Previously Presented) The method according to claim 5, comprising designating said active physical areas using a counter and incrementing the counter on each change of active area.
15. (Previously Presented) The method according to claim 6, comprising designating said active physical areas using a counter and incrementing the counter on each change of active area.
16. (Previously Presented) The method according to claim 5, comprising associating at least one bit with a logical area to represent the use state of at least one physical area of said logical area.

17. (Previously Presented) The method according to claim 6, comprising associating at least one bit with a logical area to represent the use state of at least one physical area of said logical area.
18. (Previously Presented) The method according to claim 7, comprising associating at least one bit with a logical area to represent the use state of at least one physical area of said logical area.
19. (Previously Presented) The method according to claim 5, wherein if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure, the write is carried out in an active physical area and in a blank physical area otherwise.
20. (Previously Presented) The method according to claim 6, wherein if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure, the write is carried out in an active physical area and in a blank physical area otherwise.
21. (Previously Presented) The method according to claim 7, wherein if the content of the logical area is identical to the content of the active physical area or when said write involves no erasure, the write is carried out in an active physical area and in a blank physical area otherwise.
22. (Previously Presented) The method according to claim 21, comprising programming only a portion of the logical area in the blank physical area.
23. (CANCEL)
24. (CANCEL)
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32. (Previously Presented) The method of claim 1, wherein each physical area has a status which is one of three statuses: blank, active and used.

33. (Previously Presented) The method of claim 32, wherein:

the blank status corresponds to one of the physical areas ready to receive data but not selected for receiving data,

the active status corresponds to one of the physical areas ready to receive data and selected for receiving data or to one of the physical areas containing the actual content of the logical area to be read,

the used status corresponds to one of the physical areas containing an outdated data that shall not be read, said physical area waiting for an erasure.

34. (Cancel)

35. (Cancel)

36. (Cancel)

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39. (Cancel)

40. (Cancel)

41. (Cancel)